

harry the ASIC guy "Verification Methodology" Poll Final Results - Feb 10, 2009

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	VMM	OVM	AVM	RVM	eRM	Teal / Truss	Home Grown	Native System Verilog	Native Vera	Native e	Native VHDL	Native Verilog	Other
52		OK			OK								
53	OK						OK	OK				OK	
54	OK							OK					
55		OK						OK					
56						OK							
57	OK			OK				OK	OK				
58		OK			OK								
59		OK						OK				OK	
60							OK						OK
61						OK							
62						OK							
63		OK						OK					OK
64	OK							OK				OK	
65		OK	OK		OK		OK				OK		
66	OK							OK					
67						OK							
68						OK							
69		OK						OK				OK	
70		OK			OK		OK	OK					
71	OK							OK					
72	OK	OK			OK								
73							OK				OK		OK
74		OK			OK		OK						
75		OK											
76					OK					OK	OK		
77	OK	OK						OK			OK		
78		OK						OK					
79		OK						OK					
80		OK	OK					OK			OK	OK	
81	OK							OK				OK	
82		OK											
83							OK	OK					
84							OK						
85				OK			OK	OK			OK		OK
86	OK				OK								
87		OK	OK										
88	OK								OK			OK	
89							OK				OK		
90		OK											
91	OK				OK					OK			
92		OK											
93		OK					OK	OK				OK	OK
94						OK		OK					
95							OK						OK
96						OK	OK	OK				OK	
97	OK						OK						OK
98	OK												
99							OK					OK	OK
100	OK	OK						OK				OK	OK
101	OK	OK											
102		OK						OK					OK

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	VMM	OVM	AVM	RVM	eRM	Teal / Truss	Home Grown	Native System Verilog	Native Vera	Native e	Native VHDL	Native Verilog	Other
154		OK						OK				OK	
155		OK					OK	OK					OK
156	OK								OK				
157	OK							OK					
158		OK						OK			OK		
159								OK					
160											OK		OK
161								OK				OK	
162		OK						OK					
163	OK	OK					OK	OK					OK
164		OK											
165							OK	OK				OK	
166		OK											
167		OK				OK							
168					OK		OK			OK		OK	
169	OK						OK	OK					
170	OK	OK	OK					OK					
171	OK			OK				OK	OK				
172	OK	OK			OK								
173	OK												
174	OK												
175	OK												
176	OK												
177	OK												
178													OK
179		OK						OK					
180			OK					OK			OK		
181		OK						OK			OK		
182	OK					OK		OK					
183	OK							OK				OK	
184		OK						OK				OK	
185		OK						OK			OK		
186	OK			OK				OK	OK				
187	OK	OK				OK				OK			
188		OK											
189							OK	OK					
190		OK											
191		OK			OK			OK		OK			
192		OK	OK				OK	OK			OK	OK	
193	OK							OK				OK	
194		OK					OK	OK				OK	
195		OK			OK								
196		OK						OK			OK	OK	OK
197		OK					OK	OK				OK	
198		OK					OK	OK				OK	
199		OK									OK		
200		OK						OK					
201		OK						OK				OK	
202		OK			OK	OK				OK			
203	OK												
204		OK			OK			OK		OK			

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	VMM	OVM	AVM	RVM	eRM	Teal / Truss	Home Grown	Native System Verilog	Native Vera	Native e	Native VHDL	Native Verilog	Other
205	OK					OK							
206							OK	OK				OK	
207		OK			OK					OK			
208		OK				OK		OK					
209	OK												
210	OK											OK	
211		OK			OK								
212	OK	OK	OK										
213					OK								
214		OK			OK								OK
215		OK											
216												OK	
217		OK			OK								
218		OK			OK								
219	OK												
220		OK	OK			OK	OK	OK			OK		OK
221		OK											
222		OK											
223								OK				OK	
224		OK											
225		OK						OK					
226	OK							OK	OK			OK	
227		OK	OK					OK			OK	OK	
228		OK											
229		OK											
230		OK			OK					OK			
231		OK											
232		OK			OK					OK			
233		OK			OK					OK			
234		OK											
235	OK						OK	OK					
236	OK												
237		OK	OK					OK					
238					OK								
239		OK			OK			OK		OK			
240		OK			OK					OK			
241		OK											
242		OK						OK					
243		OK											
244		OK											
245		OK			OK								
246		OK						OK			OK		
247	OK							OK					OK
248							OK	OK				OK	
249							OK				OK		
TOT	81	134	24	12	47	23	47	118	15	21	36	56	32